

CMPE 310

Labs 8 – 12

Board Design Project

Board design project reference

Assignment 5 description

Capture CIS Tutorial

PCB Editor (Allegro) Tutorial

Project Schedule

Time Table for Main Project	
Schedule	Deliverables
10/18/2019 - 10/25/2019	Interfacing Clock Generator (8284), Memory - CMOS Flash Memory and SRAM
10/25/2019 - 11/1/2019	Interfacing PPI (82C55), 8279 (Keyboard Interface), Push Buttons, PIT (8254), 7-seg LEDs, and DIP switches
11/1/2019 - 11/8/2019	Interfacing LCD Display, UART (16550), MAX 235, DSUB-9, and PIC (8259)
11/8/2019 - 11/15/2019	Final board layout, routing, DRC checks, test reports and BoM generation
11/15/2019 - 11/18/2019	Report/ Board user guide preparation and project submission